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Patent PD-202130

Customer No. 020991



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Ernest C. Chen, et al.:

Serial No.: 10/532,632 Filed: April 25, 2005

For: LOWER COMPLEXITY

LAYERED MODULATION SIGNAL PROCESSOR

Georgann S. Grunebach

22313-1450, on the date below.

Typed/or printed name of person signing certificate

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:

Mail Stop Amendment, Commissioner for Patents, P O Box 1450, Alexandria. VA

Group Art Unit: 2611

Examiner: BAYARD, Emmanuel

Date: November 22, 2006

## INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The references cited on the enclosed form PTO/SB/08A may be relevant or material to the examination of the above-identified patent application and are, therefore, submitted in compliance with the duty of disclosure defined in 37 CFR 1.56.

It is respectfully requested that the references be considered and made of record in the application identified above.

This statement should be considered because it is being submitted before the mailing of a first Office action on the merits under 37 C.F.R. §1.97(b)(3). Accordingly, it is submitted that no fee is due in this matter.

However, if it is determined that any appropriate fee is due, the Commissioner is hereby authorized to charge Deposit Account No. 50-0383 of Hughes Electronics Corporation, El Segundo, California.

This Information Disclosure Statement is not to be construed as a representation that a search has recently been made, that additional information material to the examination of this application does not exist, or that this citation constitutes prior art.

Respectfully submitted,

Bv:

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November 22, 2006

Substitute for form PTO/SB/08A	Complete if Known		
INFORMATION DISCLOSURE	Application Number	10/532,632	
NOV 2 4 2006	Filing Date	April 25, 2005	
	First Named Inventor	Ernest Chen	
	Art Unit	2611	
	Examiner Name	BAYARD, Emmanuel	
Date: S/ November 22, 2006			
Date: 8 November 22, 2006	Attorney Docket Number	PD-202130	

	U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	
		Number-Kind Code	MM-DD-YYYY		or Relevant Figures Appear	
		US-2002/006795	01-2002	Norin et al.		
		US-6,333,924	12-2001	Porcelli et al.		
		US-2001/012322	08-2001	Nagaoka et al.		
			<del></del>			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.	Foreign Patent Document  Country Code-Number-Kind  Code	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
	ļ 1	WO 2005/086444	09-2005	Thomson Licensing S.A.		
		WO 2005/074171	08-2005	Qualcomm Incorporated	·	

Examiner Signature	Date Considered	

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.